She t1 of 1 PTO-1449 (SUBSTITUTE) Applic. No. \* Attorney Docket No.: GR 99 P 1679 10/033,227 U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE **Applicant** Gerald Deboy et al. INFORMATION DISCLOSURE STATEMENT BY APPLICANT Filing Date Group Art Unit (37 CFR 1.98(b)) October 22, 2001 2812 **U.S. PATENT DOCUMENTS EXAMINER** SUB FILING CLASS INITIALS CLASS PATENT NO. DATE **PATENTEE** DATE Α 4,811,075 03/07/89 Eklund В 5,170,241 Yoshimura et al. 12/08/92 C 5,285,369 02/08/94 Balakrishnan D 5,296,725 03/22/94 Nandakumar et al. 5,313,082 E 05/17/94 Eklund F G H FOREIGN PATENT DOCUMENT

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KR	0	G. Deboy et al.: "A new generation of high voltage MOSFETs breaks the limit line of silicon", IEDM 98, pp. 683-685, XP-000859463					
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